

Amendments to the Claims

A complete set of the existing claims are set forth below, with the amended claims showing deletions (~~strike-through~~) and insertions (underline).

1. (Currently Amended) An apparatus comprising:
an integrated circuit (IC) having
an operational circuit to operate at a first frequency, and
a proxy circuit to output a periodic signal at a second frequency reflective
of a potential of the first frequency; and
a voltage regulator controller coupled to the proxy circuit to receive the periodic
signal, and conditionally regulate the voltage applied to the IC, based at least in
part on the second frequency.
2. (Original) The apparatus of claim 1, wherein at least a portion of the voltage
regulator controller is part of the IC.
3. (Original) The apparatus of claim 1, wherein at least a portion of the voltage
regulator is part of the IC and the apparatus is in the IC.
4. (Original) The apparatus of claim 1, wherein the proxy circuit comprises a ring
oscillator.
5. (Original) The apparatus of claim 1, wherein the voltage regulator controller
comprises a monitor coupled to the proxy circuit to receive the periodic signal,
determine and output a difference signal indicative of an amount of difference between
the second frequency and a target of the second frequency.
6. (Original) The apparatus of claim 5, wherein

the apparatus further comprises a first storage to store the target of the second
frequency; and the monitor comprises

a first counter coupled to the proxy circuit to receive a first derived version of the
periodic signal for a period of time, determine the second frequency based

- at least in part on the first derived version of the periodic signal received during the period of time, and output a first count value, indicative of the determined second frequency for the period; and
- a register coupled to the first counter to store the first count value, and output the stored first count value as the determined second frequency; and
- a comparator coupled to the register and the storage to receive the determined second frequency and the target of the second frequency, and to generate and output the difference signal.
7. (Original) The apparatus of claim 6, wherein the monitor further comprises a divider coupled to the proxy circuit to receive the periodic signal and to generate a second derived version of the periodic signal; and
- a synchronizer coupled to the divider and the first counter to receive the second derived version of the periodic signal and a clock signal, time adjust the second derived version of the periodic signal to form said first derived version of the periodic signal, and to provide the first derived version of the periodic signal to the first counter.
8. (Original) The apparatus of claim 7, wherein the monitor further comprises a second counter to receive the clock signal, and outputs a third count value for the first counter and the register to control their operations.
9. (Original) The apparatus of claim 6, wherein the apparatus further comprises a second storage to store an adjustment to the target of the second frequency, and the monitor further bases its output of the difference signal on the adjustment to the target of the second frequency.
10. (Original) The apparatus of claim 6, wherein the apparatus further comprises a second storage to store an upward adjustment to the target of the second frequency, and third storage to store a downward adjustment to the target of the second frequency, and the voltage regulator controller further bases its output of the difference signal on

the upward adjustment as well as the downward adjustment to the target of the second frequency.

11. (Original) The apparatus of claim 10, wherein the IC comprises a storage unit, and at least one of the first, second, and third storages are different storage locations of the storage unit.

12. (Original) The apparatus of claim 11, wherein the storage unit is a selected one of a read-only-memory, and a random access memory.

13. (Original) The apparatus of claim 10, wherein at least a selected one of the first, second, and third storages comprises one or more fuses.

14. (Original) The apparatus of claim 5, wherein the voltage regulator controller further comprises a control block coupled to the monitor to receive the difference signal, and generate one or more voltage application control signals to control voltage applied to the IC, based at least in part on the received difference signal.

15. (Currently Amended) The apparatus of claim 14, wherein at least one of the voltage application control signals controls a pulse width of a voltage signal outputted by a transistor of a device to ~~affect~~ effect a current outputted by the device.

16. (Original) The apparatus of claim 14, wherein the control block comprises a microcontroller.

17. (Original) In an apparatus including an integrated circuit (IC) having an operational circuit designed to operate at a first frequency, a method comprising:
generating a periodic signal at a location on the IC near the operational circuit ,
the periodic signal having a second frequency reflective of a potential of the first frequency; and

monitoring the periodic signal to determine the second frequency;
generating a difference signal indicating an amount of difference between the second frequency and the a target second frequency; and

regulating voltage being applied to the IC, based at least in part on the amount of difference.

18. (Original) The method of claim 17, wherein said generating further comprises applying an adjustment to the target of the second frequency.

19. (Original) The method of claim 17, wherein said generating further comprises applying an upward adjustment and a downward adjustment to the target of the second frequency.

20. (Currently Amended) The method of claim 17, said regulating comprises generating one or more control signals to control a number of electronic elements of a device to ~~affect~~ effect voltage being applied to the IC, based at least in part on the amount of the difference.

21. (Original) A system comprising:

a first microprocessor having

a first operational circuit to operate at a first frequency; and

a first proxy circuit to output a first periodic signal at a second frequency
reflective of a potential of the first frequency;

a first voltage regulator controller, either coupled to, or integrated with the first microprocessor, to receive the first periodic signal, and conditionally regulate voltage applied to the first microprocessor, based at least in part on the second frequency;

a bus coupled to the first microprocessor;

a networking interface coupled to the bus.

22. (Original) The system of claim 21, wherein at least a portion of the first voltage regulator controller is integrated with the microprocessor.

23. (Original) The system of claim 21, wherein the first proxy circuit comprises a ring oscillator.

24. (Original) The system of claim 21, wherein the first voltage regulator controller comprises a monitor coupled to the first proxy circuit to receive the first periodic signal, and determine and output a difference signal indicative of an amount of difference between the second frequency and a target of the second frequency.

25. (Original) The system of claim 24, wherein
the system further comprises a first storage to store the target of the second frequency; and
the monitor comprises
a first counter coupled to the first proxy circuit to receive a first derived version of the first periodic signal for a period of time, determine the second frequency based at least in part on the first derived version of the first periodic signal received during the period of time, and output a first count value, indicative of the determined second frequency for the period,
a register coupled to the first counter to store the first count value, and output the stored first count value as the determined second frequency, and
a comparator coupled to the register and the storage to receive the determined second frequency and the target of the second frequency, and to generate and output the difference signal.

26. (Original) The system of claim 25, wherein the monitor further comprises a divider coupled to the first proxy circuit to receive the first periodic signal, and generate a second derived version of the first periodic signal, and a synchronizer coupled to the divider and the first counter, to receive the second derived version of the first periodic signal and a clock signal, to time adjust the second derived version of the first periodic signal, and to provide the time adjusted second derived version of the first periodic signal as the first derived version of the first periodic signal to the first counter.

27. (Original) The system of claim 26, wherein the monitor further comprises a second counter coupled to the first counter and the register to receive a clock signal

and to output a control signal to control the operations of the first counter and the register.

28. (Original) The system of claim 25, wherein the system further comprises a second storage to store an adjustment to the target of the second frequency, and the monitor further bases its output of the difference signal on the adjustment to the target of the second frequency.

29. (Original) The system of claim 25, wherein the system further comprises a second storage to store an upward adjustment to the target of the second frequency, and third storage to store a downward adjustment to the target of the second frequency, and the monitor further bases its output of the difference signal on the upward adjustment as well as the downward adjustment to the target of the second frequency.

30. (Original) The system of claim 24, wherein the first voltage regulator controller further comprises a control block coupled to the monitor to receive the difference signal, and generate one or more control signals to control electronic elements of a device to affect said voltage being applied to said IC, based at least in part on the received difference signal.

31. (Original) The system of claim 21, wherein the system further comprises a second microprocessor having
a second operational circuit to operate at a third frequency; and
a second proxy circuit to output a second periodic signal at a fourth frequency reflective of a potential of the second frequency; and
a second voltage regulator controller, either coupled to, or integrated with the second microprocessor, to receive the second periodic signal, and conditionally regulate voltage applied to the second microprocessor, based at least in part on the fourth frequency.

32. (Original) The system of claim 31, wherein the first proxy circuit and the first voltage regulator of the first microprocessor, and the second proxy circuit and the

second voltage regulator of the second microprocessor are configured in a coordinated manner